

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Present Application:**

Applicants : Vito Fabbri et al.  
Title : LOW-VOLTAGE, VERY-LOW-POWER CONDUCTANCE MODE  
NEURON  
Docket No. : 850063.603RI  
Date : July 31, 2003

**Prior Application:**

Examiner : Mark R. Powell  
Art Unit : 2122  
Application No. : 09/461,674

Mail Stop Reissue  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Commissioner for Patents:

Please amend the above-identified application as follows:

**In the Claims:**

Please add new claims 7-24 to read as follows:

7. (New) A network comprising:

a plurality of synaptic weighting elements organized in a first set and a second set, each set having a respective programmable conductance, each of the synaptic weighting elements having a respective synaptic input connection for receiving an input signal and an output connection;